# JK-mSMD260-16 PPTC DEVICES

**Part Number:** Q/JKTD-16-260

Edition: A0

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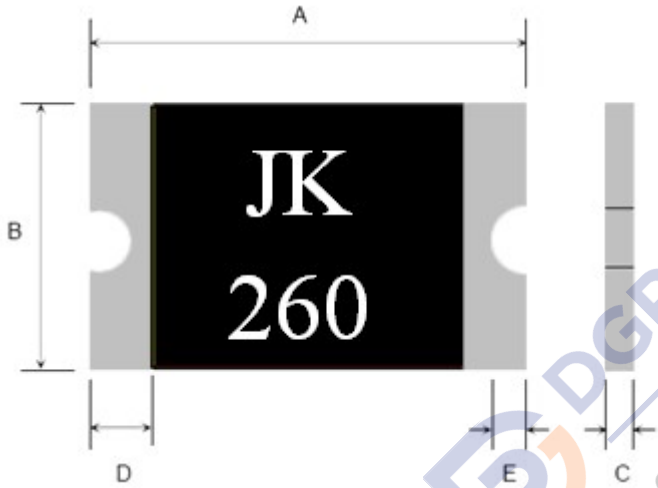
金瑞电子材料
Jinrui Electronic material




Terminal pad materials :Tin-Plated Nickle-copper

Terminal pad solderability : Meets EIA specification RS 186-9E and ANSI/J-STD-002 Category 3.

Marking : JK260=1812(260)



Table1 :DIMENTION(Unit : mm)

| Model | Marking | A | | B | | C | | D | E |
|---|---|---|---|---|---|---|---|---|---|
| | | Min. | Max. | Min. | Max. | Min. | Max | Min. | Min |
| JK-mSMD260-16 | JK260 | 4.37 | 4.73 | 3.07 | 3.41 | 0.90 | 1.80 | 0.30 | 0.25 |

Table2 :PERFORMANCE RATINGS:

| Model | $V_{max}$ | $I_{max}$ | $I_{hold}$ @25℃ | $I_{trip}$ @25℃ | $P_d$ Typ | Maximum Time To Trip | | Resistance | | |
|---|---|---|---|---|---|---|---|---|---|---|
| | | | | | | Current | Time | $Ri_{min}$ | $Ri_{typ}$ | $R1_{max}$ |
| | (Vdc) | (A) | (A) | (A) | (W) | (A) | (Sec) | (Ω) | (Ω) | (Ω) |
| JK-mSMD260-16 | 16 | 100 | 2.60 | 5.00 | 0.8 | 8.0 | 2.50 | 0.015 | 0.027 | 0.060 |

Table3:Test Conditons and Standards

| Item | Test Conditon | Standard |
|---|---|---|
| Initial Resistance | 25℃ | 0.015～0.060Ω |
| $I_H$ | 25℃，2.60A， 60min | No Trip |
| Ttrip | 25℃，8.0A | ≤2.50s |
| Trip endurance | 16V，100A， 1hr | No arcing or burning |

**Operating Temperature：-40℃ TO 85℃**

**Packaging：Bulk ,1500pcs per bag**

**SHENZHEN JINRUI ELECTRONIC MATERIAL CO.,LTD 6 F DISTRICT NO. 3000046 BLDG Hi-Tech SCIENCE & INDUSTRY PARK SHANGKENG COMMUNITY GUANLAN STREET BAOAN SHENZHEN**

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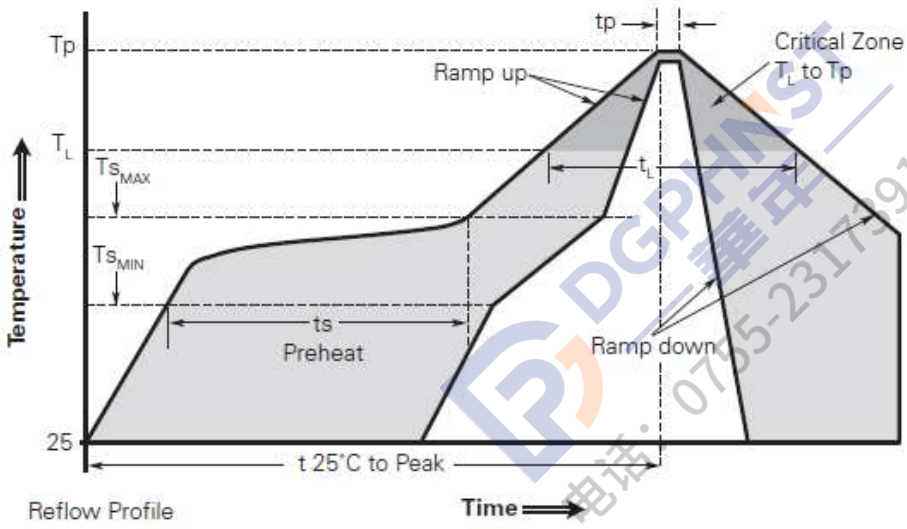
**Solder reflow conditions**




| Profile Feature | Pb-Free Assembly |
|---|---|
| **Average ramp up rate ($Ts_{MAX}$ to Tp)** | 3°C/second max. |
| **Preheat** | |
| • Temperature min. ($Ts_{MIN}$) | 150°C |
| • Temperature max. ($Ts_{MAX}$) | 200°C |
| • Time ($ts_{MIN}$ to $ts_{MAX}$) | 60-120 seconds |
| **Time maintained above:** | |
| • Temperature ($T_L$) | 217°C |
| • Time ($t_L$) | 60-150 seconds |
| **Peak/Classification temperature (Tp)** | 260°C |
| **Time within 5°C of actual peak temperature** | |
| Time (tp) | 30 seconds max. |
| **Ramp down rate** | 3°C/second max. |
| **Time 25°C to peak temperature** | 8 minutes max. |

**Note:** All temperatures refer to topside of the package, measured on the package body surface.

- Recommended reflow methods: IR, vapor phase oven, hot air oven, N2 environment for lead-free.
- Devices are not designed to be wave soldered to the bottom side of the board.
- Recommended maximum paste thickness is 0.25mm (0.010inch).
- Devices can be cleaned using standard industry methods and solvents.
- Soldering temprature profile meets RoHs leadfree process.

Notes: If reflow temperatures exceed the recommended profile, devices may not meet the performance requirements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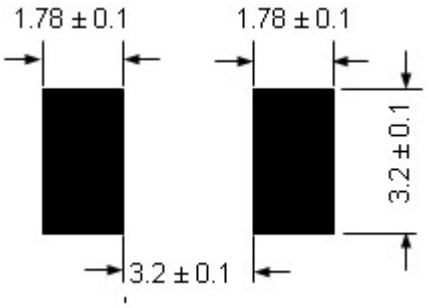
**Recommended pad layout (mm)**




**WARNING**

· Use PPTC beyond the maximum ratings or improper use may result in device damage and possible electrical arcing and flame.

· PPTC are intended for protection against occasional over current or over temperature fault conditions and should not be used when repeated fault conditions or prolonged trip events are anticipated.

· Device performance can be impacted negatively if devices are handled in a manner inconsistent with recommended electronic, thermal, and mechanical procedures for electronic components.

· Use PPTC with a large inductance in circuit will generate a circuit voltage (L di/dt) above the rated voltage of the PPTC.

· Avoid impact PPTC device its thermal expansion like placed under pressure or installed in limited space.

· Contamination of the PPTC material with certain silicon based oils or some aggressive solvents can adversely impact the performance of the devices.PPTC SMD can be cleaned by standard methods.

· Requests that customers comply with our recommended solder pad layouts and recommended reflow profile. Improper board layouts or reflow profilecould negatively impact solderability performance of our devices.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