



JX4S0080R120M

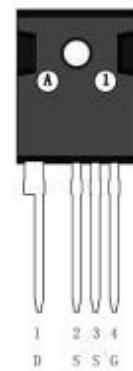
1200V N-Channel MOSFET

### Description

Silicon Carbide (SiC) MOSFET use a completely new technology that provide superior switching performance and higher reliability compared to Silicon. In addition, the low ON resistance and compact chip size ensure low capacitance and gate charge. Consequently, system benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size.

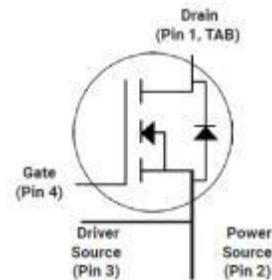
### Features

- High Speed Switching with Low Capacitances
- High Blocking Voltage with Low RDS(on)
- Optimized package with separate driver source pin
- Easy to parallel and simple to drive
- ROHS Compliant, Halogen free



### Application

- EV Charging
- High Voltage DC/DC Converters
- Switch Mode Power Supplies
- Power Factor Correction Modules



### Ordering Information

Part Number	Marking	Package	Packaging
JX4S0080R120M	JX4S0080R120M	TO-247	Tube



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1200V N-Channel MOSFET

Absolute Maximum Ratings( $T_c=25^\circ C$ )

Symbol	Parameter	Value	Unit
$V_{DS}$	Drain-Source Voltage	1200	V
$I_D$	Drain Current(continuous)at $T_c=25^\circ C$	32	A
$I_D$	Drain Current(continuous)at $T_c=100^\circ C$	23	A
$I_{DM}$	Drain Current (pulsed)	80	A
$V_{GS}$	Gate-Source Voltage	-10/+22	V
$P_D$	Power Dissipation $T_c = 25^\circ C$	145	W
$T_J, T_{stg}$	Junction and Storage Temperature Range	-55 to +150	° C

Electrical Characteristics( $T_J = 25^\circ C$  unless otherwise specified)

Typical Performance-Static

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$BV_{DS}$	Drain-source Breakdown Voltage	$I_D=250\mu A, V_{GS}=0V$	1200			V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=1200V, V_{GS}=0V, T_J=25^\circ C$			100	$\mu A$
$I_{GSS}$	Gate-body Leakage Current	$V_{DS}=0V ; V_{GS}=-10 \text{ to } 20V$			250	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}= V_{GS}, I_D=5mA$	2	3	4	V
$V_{GS(on)}$	Recommended turn-on Voltage	Static		20		V
$V_{GS(off)}$	Recommended turn-off Voltage			-5		V
$R_{DS(on)}$	Static Drain-source On Resistance	$V_{GS}=20V, I_D=20A$		80	100	$m\Omega$
		$V_{GS}=20V, I_D=20A, T_J=150^\circ C$		122		$m\Omega$



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### Typical Performance-Dynamic

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$C_{iss}$	Input Capacitance	$V_{DS}=1000V, f=1MHz$ , $V_{AC}=25mV$		1590		pF
$C_{oss}$	Output Capacitance			63		pF
$C_{rss}$	Reverse Transfer Capacitance			3.9		pF
$g_{fs}$	Transconductance	$V_{DS}=20V, I_D=20A$		12		S
$E_{oss}$	Coss Stored Energy	$V_{DS}=1000V, f=1MHz$		35.7		$\mu J$
$E_{ON}$	Turn-On Energy (Body Diode)	$V_{DS}=800V, V_{GS}=-5/20V, I_D=20A, L=150\mu H$		455		$\mu J$
$E_{OFF}$	Turn-Off Energy (Body Diode)	$T_J=150^{\circ}C$		111		$\mu J$
$Q_g$	Total Gate Charge	$V_{DS}=800V, V_{GS}=-5V/20V, I_D = 15 A$		72		nC
$Q_{gs}$	Gate-source Charge			21		nC
$Q_{gd}$	Gate-Drain Charge			22		nC
$R_{G(int)}$	Internal Gate Resistance	$f=1MHz, V_{AC}=25mV$		3		$\Omega$
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=800V, V_{GS}=-5V/20V, I_D = 20A, L=150 \mu H$ $R_{ext}=2.5\Omega$		42		ns
$t_r$	Rise Time			15		ns
$t_{d(off)}$	Turn-off Delay Time			36		ns
$t_f$	Fall Time			11		ns

### Typical Performance-Reverse Diode( $T_J = 25^{\circ}C$ unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_{FSD}$	Forward Voltage	$V_{GS}=0V, I_F=10A, T_J=25^{\circ}C$		4.8	6	V
		$V_{GS}=0V, I_F=10A, T_J=150^{\circ}C$		4.2	6	V
$I_s$	Continuous Diode Forward Current	$V_{GS}=0V, T_c=25^{\circ}C$		27		A
$t_{rr}$	Reverse Recovery Time	$V_{GS}=-5 V, I_F=20A,$		36		nS
$Q_{rr}$	Reverse Recovery Charge	$V_R=800 V, di/dt=900 A/\mu s, T_J=150^{\circ}C$		297		nC
$I_{rrm}$	Peak Reverse Recovery Current			15.5		A

### Thermal Characteristics

Symbol	Parameter	Value.	Unit
$R_{0JC}$	Thermal Resistance, Junction-to-Case	0.86	$^{\circ}C/W$
$R_{0JA}$	Thermal Resistance, Junction-to-Ambient	40	$^{\circ}C/W$

The values are based on the junction-to case thermal impedance which is measured with the device mounted to a large heat sink assuming maximum junction temperature of  $T_j(max)=150^{\circ}C$



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## Electrical Characteristics

Fig1. Output characteristics ( $T_J = 25^\circ\text{C}$ )

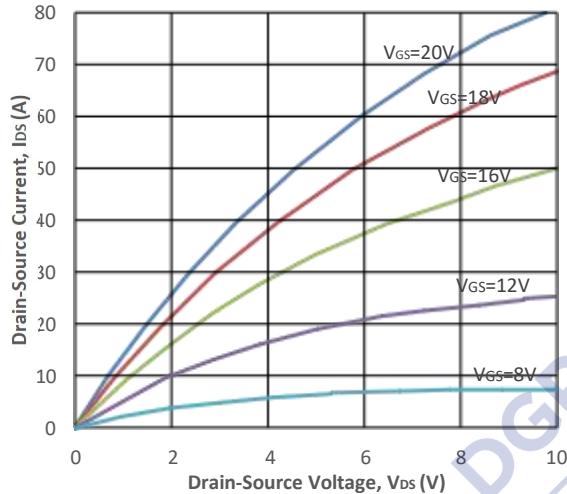


Fig2. Output characteristics ( $T_J = 150^\circ\text{C}$ )

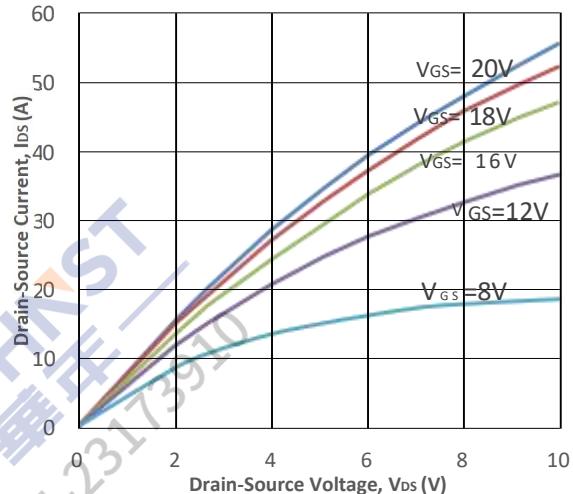


Fig3. Normalized On-Resistance vs. Temperature

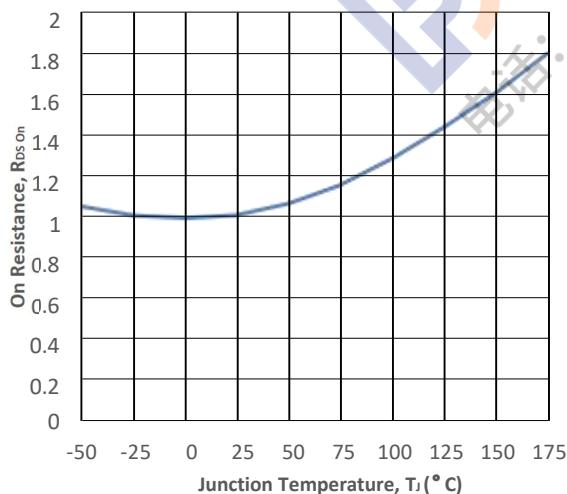


Fig5. Transfer Characteristic

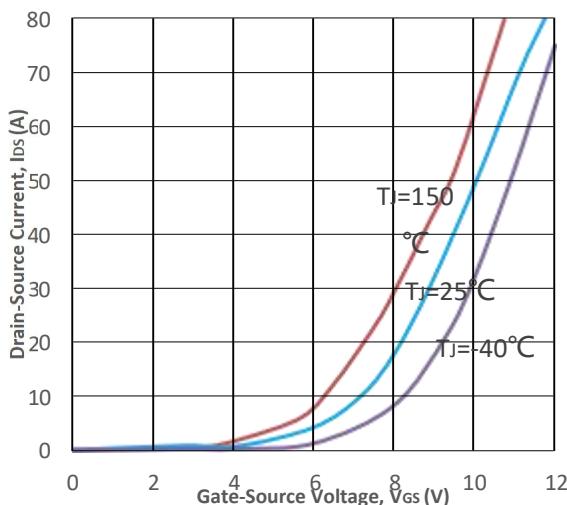


Fig4. On-Resistance vs. Temperature

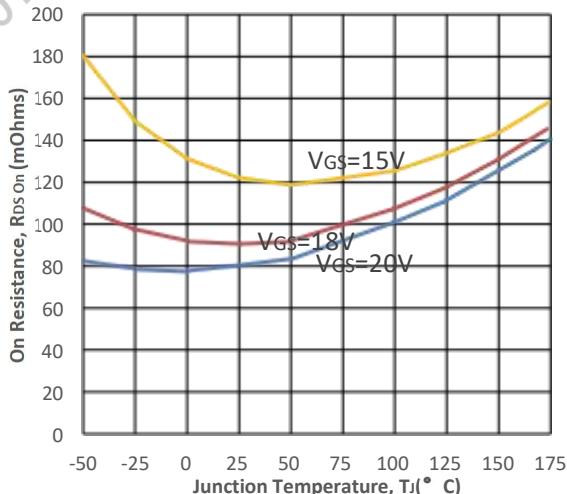
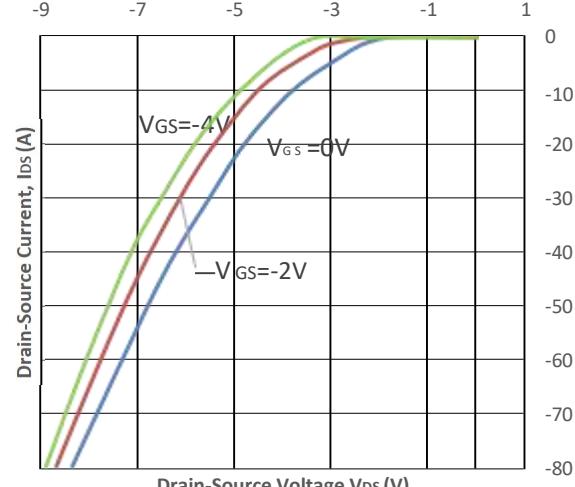
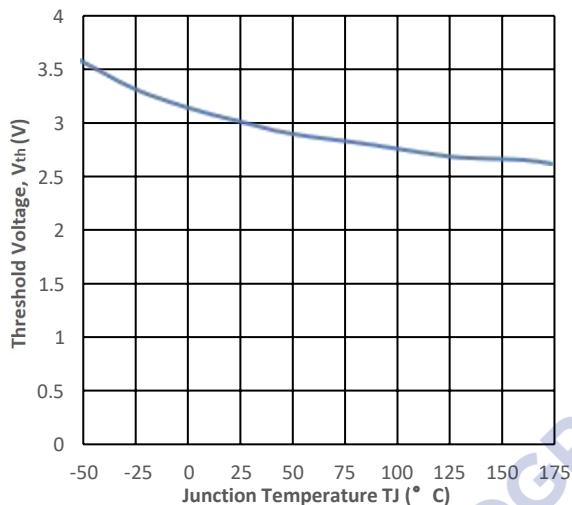
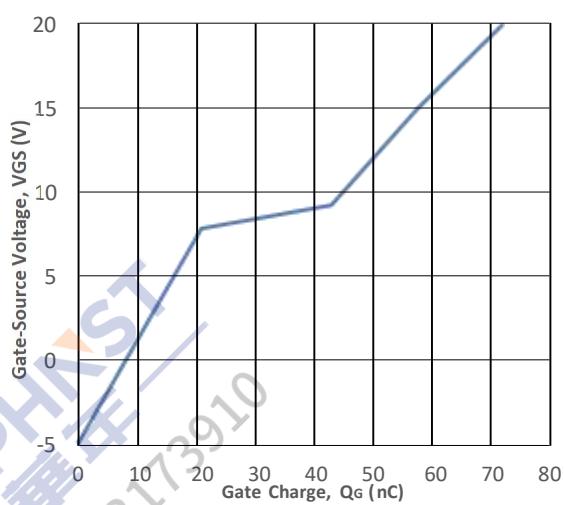
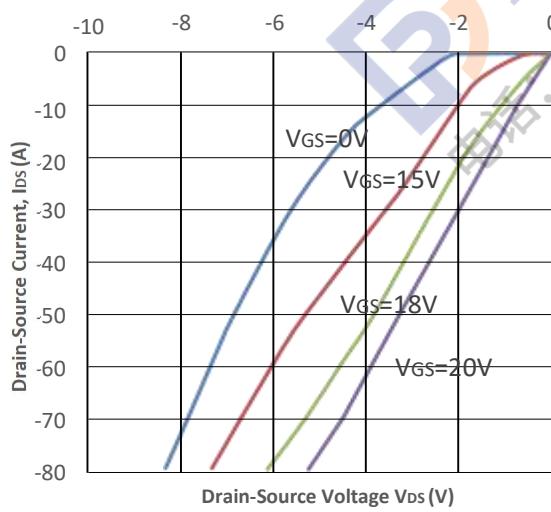
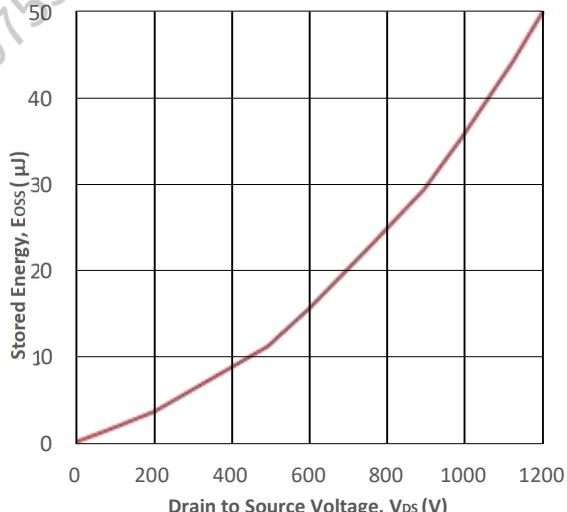
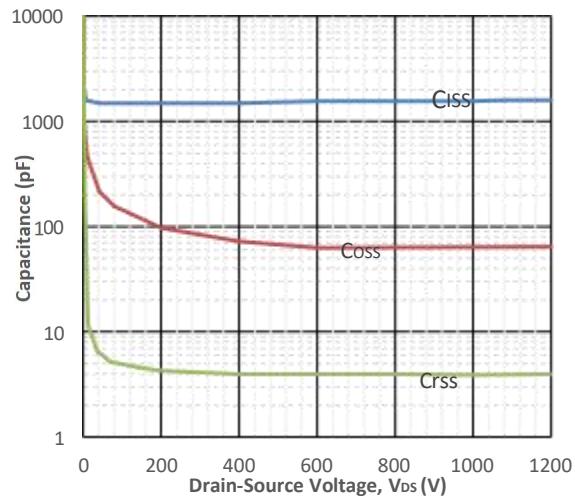
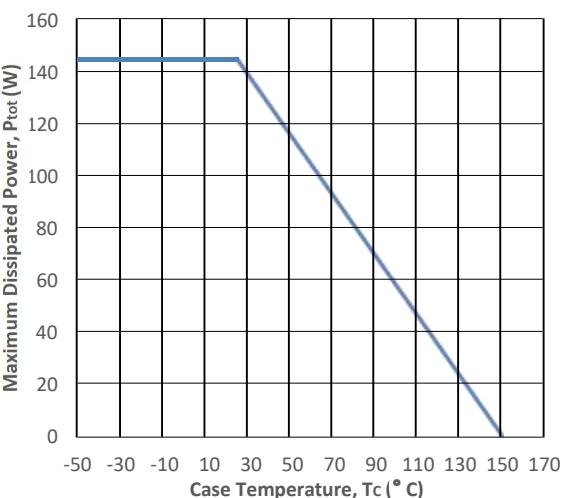
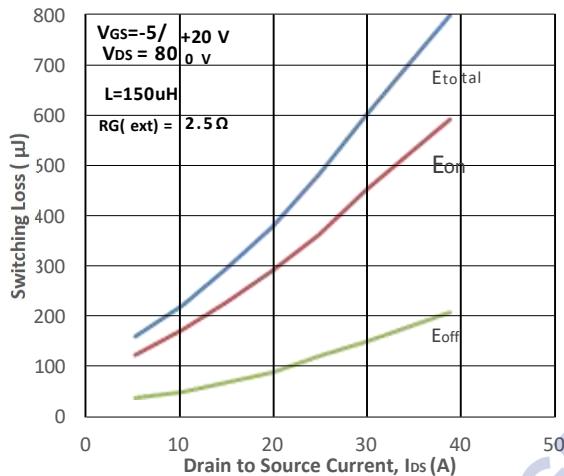
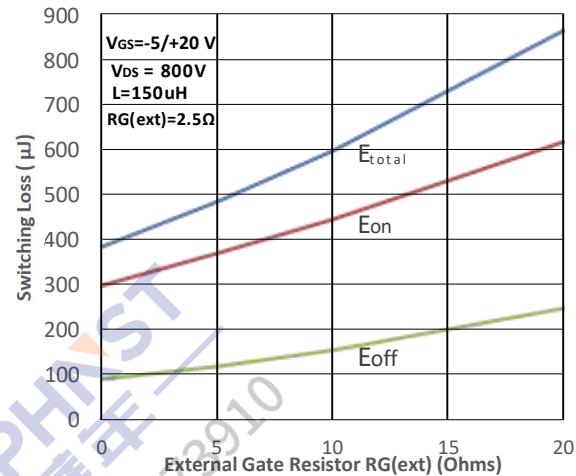
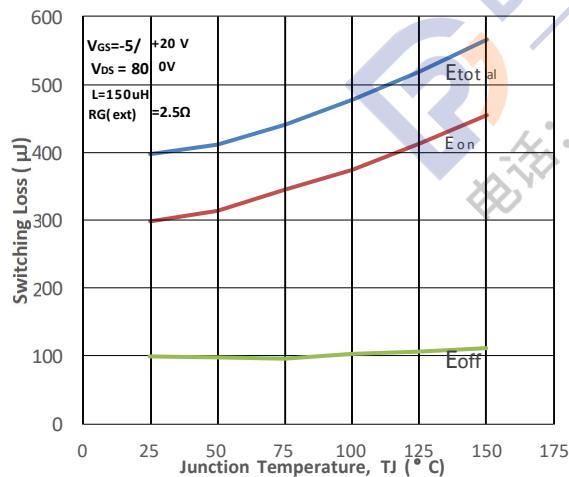
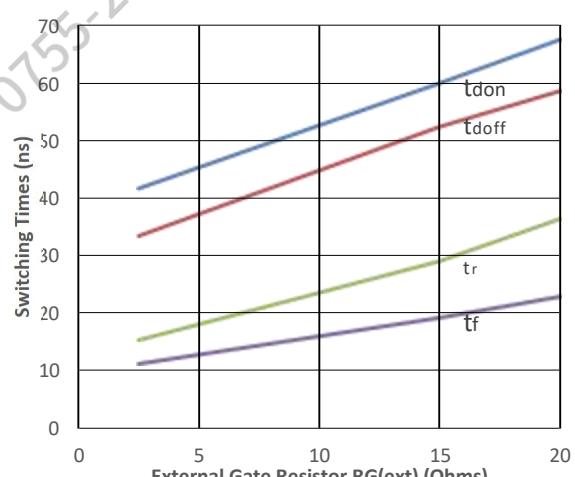
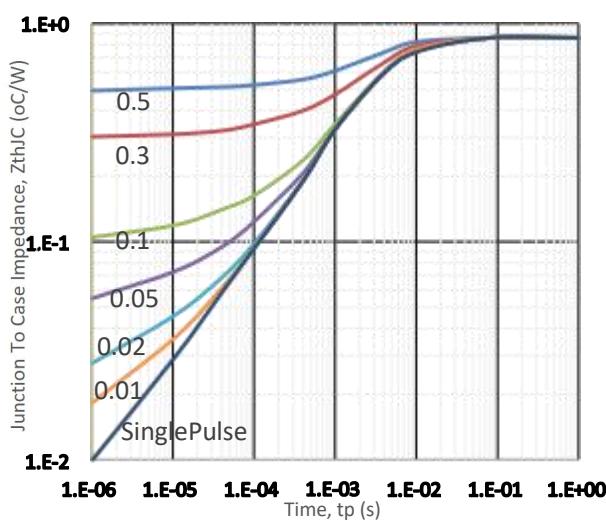
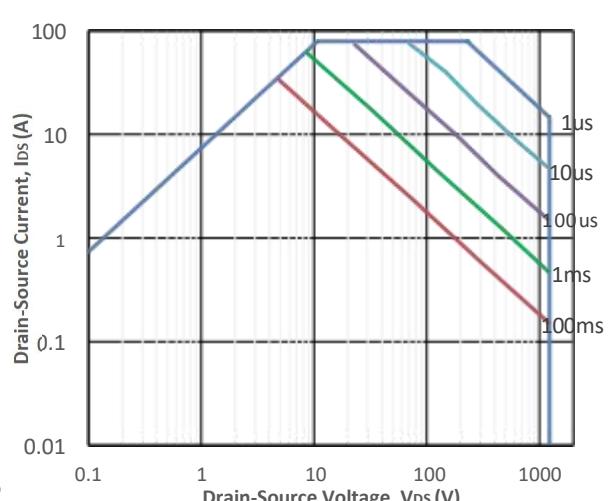


Fig6. Body Diode Characteristic at  $25^\circ\text{C}$



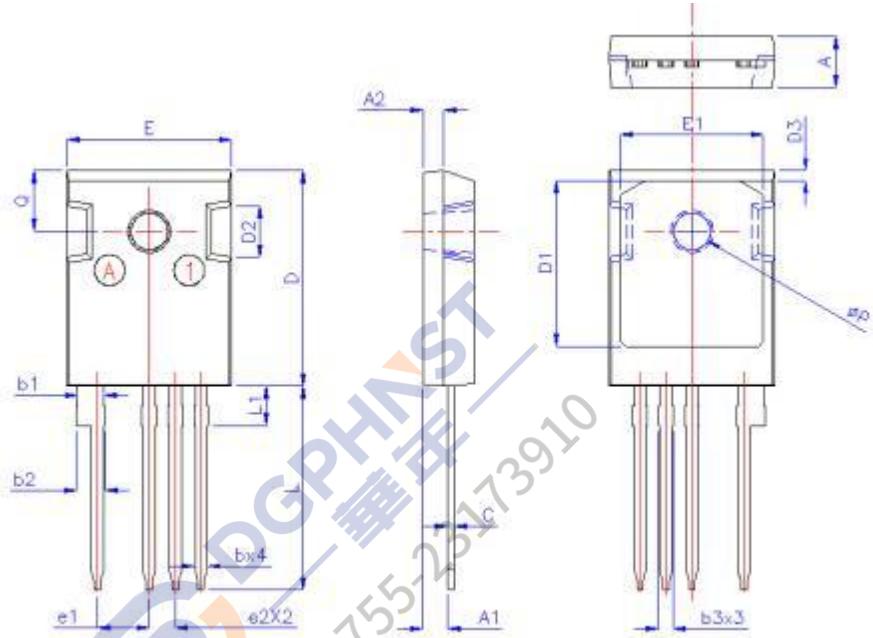
**Fig7.Threshold Voltage vs. Temperature**

**Fig8. Gate Charge Characteristics**

**Fig9. 3rd Quadrant Characteristic at 25 °C**

**Fig10. Output Capacitor Stored Energy**

**Fig11. Capacitances vs. Drain-Source**

**Fig12. Max Power Dissipation Derating Vs Tc**


**Fig13. Switching Energy vs. Drain Current**

**Fig14. Switching Energy vs. RG(ext)**

**Fig15. Switching Energy vs. Temperature**

**Fig16. Switching Times vs. RG(ext)**

**Fig17. Transient Thermal Impedance**

**Fig18. Safe Operating Area**




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Package Drawing:



Dimensions ( UNIT: mm)

SYMBLDS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	4.9	5	5.1	0.193	0.197	0.201
A1	2.31	2.42	2.52	0.091	0.095	0.099
A2	1.9	2	2.1	0.075	0.079	0.083
b	1.16	1.22	1.27	0.046	0.048	0.050
b1	1.15	1.2	1.25	0.045	0.047	0.049
b2	2.61	2.76	2.91	0.103	0.109	0.115
b3	1.36	1.42	1.47	0.054	0.056	0.058
C	0.59	0.62	0.66	0.023	0.024	0.026
D	20.9	21	21.1	0.823	0.827	0.831
D1	15.94	16.24	16.54	0.628	0.639	0.651
D2		5			0.197 TYP	
D3	0.8	0.95	1.1	0.031	0.037	0.043
e	5.08 BSC				0.200 BSC	
e1	2.54 BSC				0.100BSC	
E	16.05	16.15	16.25	0.632	0.636	0.640
E1	13.82	14.02	14.26	0.544	0.552	0.561
L	19.75	19.95	20.15	0.778	0.785	0.793
L1	---	---	3.87	---	---	0.152
Q	5.95 BSC			0.234BSC		
ØP	3.45	3.6	3.75	0.136	0.142	0.148